

===== WPI =====

TI - Memory device - has ferroelectric data storage layer on piezoelectric layer which polarises storage layer NoAbstract Dwg 1,2/14
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PN - JP4038866 A 19920210 DW199219 012pp
PR - JP19900145621 19900604
PA - (OLYU) OLYMPUS OPTICAL CO LTD
MC - U14-A03F
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IC - H01L27/10
AN - 1992-153892 [19]

===== PAJ =====

TI - MEMORY DEVICE AND ITS RECORDING/REPRODUCING METHOD
AB - PURPOSE: To increase exceedingly its capacity by installing a ferroelectric conductor layer where stress laminated on one side of a piezoelectric body layer is transmitted, a write in electrode which turns the ferroelectric conductor layer into a partial electrode condition, and a read out electrode which generates stress to such an extent which does not destroy the partial electrode condition.
- CONSTITUTION: Stripe-shaped electrodes 21 and 22 are laid out on both sides of a piezoelectric body film 20 having a piezoelectric characteristic. A ferroelectric conductor film 23 is laid out on both sides on one side of the piezoelectric body film 20 by way of an insulation film 24. On the both sides of the ferroelectric body film 23 there are laid out stripe-shaped electrodes 25 and 26 which cross each other between both ends. Moreover, the respective stripe-shaped electrodes 21, 22, 25, and 26 are laid out so that the intersection where the stripe-shaped electrode 21 formed on one side of the piezoelectric body film 20 crosses the electrode formed on the other side may respond to the intersection between the electrodes 25 and 26 on the ferroelectric film 23 on a one to one basis. A piezoelectric cell is formed in a section where the electrodes 21 and 22 on each side of the piezoelectric film 20 intersect each other while a memory cell is formed on a section where the electrodes 25 and 26 on each side of the ferroelectric body film 23 intersect each other.
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